SIEMENS

Data sheet

6ES7313-5BG04-0AB0



SIMATIC S7-300, CPU 313C, Compact CPU with MPI, 24 DI/16 DO, 4 AI, 2 AO, 1 Pt100, 3 high-speed counters (30 kHz), Integr. power supply 24 V DC, work memory 128 KB, Front connector (2x 40-pole) and Micro Memory Card required

General information	
HW functional status	01
Firmware version	V3.3
Engineering with	
 Programming package 	STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203
Supply voltage	
Rated value (DC)	24 V
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines (recommendation)	Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A
Mains buffering	
 Mains/voltage failure stored energy time 	5 ms
Repeat rate, min.	1 s
Load voltage L+	
Digital inputs	
— load voltage / at digital input / at DC / rated value	24 V
- Reverse polarity protection	Yes
Digital outputs	
— Rated value (DC)	24 V
 Reverse polarity protection 	No
Input current	
Current consumption (rated value)	650 mA
Current consumption (in no-load operation), typ.	150 mA
Inrush current, typ.	5 A
l²t	0.7 A ² ·s
Digital inputs	
 from load voltage L+ (without load), max. 	80 mA
Digital outputs	
 from load voltage L+, max. 	50 mA
Power loss	
Power loss, typ.	12 W
Memory	
Work memory	
• integrated	128 kbyte
• expandable	No
Load memory	
Plug-in (MMC)	Yes
• Plug-in (MMC), max.	8 Mbyte
 Data management on MMC (after last programming), 	10 a
min.	

Backup	
• present	Yes; Guaranteed by MMC (maintenance-free)
without battery	Yes; Program and data
CPU processing times	
for bit operations, typ.	0.07 µs
for word operations, typ.	0.15 µs
for fixed point arithmetic, typ.	0.2 µs
for floating point arithmetic, typ.	0.72 µs
CPU-blocks	
Number of blocks (total)	1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
DB	
• Number, max.	1 024; Number range: 1 to 16000
• Size, max.	64 kbyte
FB	
• Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
FC	
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
OB	
• Number, max.	see instruction list
• Size, max.	64 kbyte
 Number of free cycle OBs 	1; OB 1
 Number of time alarm OBs 	1; OB 10
 Number of delay alarm OBs 	2; OB 20, 21
 Number of cyclic interrupt OBs 	4; OB 32, 33, 34, 35
 Number of process alarm OBs 	1; OB 40
 Number of startup OBs 	1; OB 100
 Number of asynchronous error OBs 	4; OB 80, 82, 85, 87
 Number of synchronous error OBs 	2; OB 121, 122
Nesting depth	
 per priority class 	16
 additional within an error OB 	4
Counters, timers and their retentivity	
S7 counter	
Number	256
Retentivity	
— adjustable	Yes
— preset	Z 0 to Z 7
Counting range	
— lower limit	0
— upper limit	999
IEC counter	
present	Yes
• Туре	SFB
Number	Unlimited (limited only by RAM capacity)
S7 times	
Number	256
Retentivity	
— adjustable	Yes
— preset	No retentivity
Time range	
— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
present	Yes
• Туре	SFB
Number	Unlimited (limited only by RAM capacity)
Data areas and their retentivity	
Retentive data area (incl. timers, counters, flags), max.	64 kbyte

-	
Flag	
• Size, max.	256 byte
Retentivity available	Yes; MB 0 to MB 255
Retentivity preset	MB 0 to MB 15
Number of clock memories	8; 1 memory byte
Data blocks	
 Retentivity adjustable 	Yes; via non-retain property on DB
Retentivity preset	Yes
Local data	
 per priority class, max. 	32 kbyte; Max. 2048 bytes per block
Address area	
I/O address area	
Inputs	1 024 byte
Outputs	1 024 byte
of which distributed	
— Inputs	none
— Outputs	none
Process image	
Inputs	1 024 byte
Outputs	1 024 byte
 Inputs, adjustable 	1 024 byte
Outputs, adjustable	1 024 byte
Inputs, default	128 byte
Outputs, default	128 byte
Default addresses of the integrated channels	
— Digital inputs	124.0 to 126.7
— Digital outputs	124.0 to 125.7
— Analog inputs	752 to 761
— Analog outputs	752 to 755
Digital channels	
Inputs	1 016
— of which central	1 016
Outputs	1 008
— of which central	1 008
Analog channels	
Inputs	253
— of which central	253
Outputs	250
— of which central	250
Hardware configuration	
Number of expansion units, max.	3
Number of DP masters	5
integrated	none
via CP	4
Number of operable FMs and CPs (recommended)	·
FM	8
• F™I • CP, PtP	8
• CP, FIP • CP, LAN	6
• CP, LAN Rack	
	4
Racks, max. Modules per rack, max	
Modules per rack, max. Time of day	8; In rack 3 max. 7
Time of day	
	Vee
Hardware clock (real-time)	Yes
retentive and synchronizable	Yes
Backup time	6 wk; At 40 °C ambient temperature
Deviation per day, max.	10 s; Typ.: 2 s
Behavior of the clock following POWER-ON	Clock continues running after POWER OFF
 Behavior of the clock following expiry of backup period 	the clock continues at the time of day it had when power was switched off
Operating hours counter	

Number	1
Number/Number range	0
 Range of values 	0 to 2^31 hours (when using SFC 101)
Granularity	1 h
retentive	Yes; Must be restarted at each restart
Clock synchronization	
supported	Yes
• to MPI, master	Yes
• on MPI, device	Yes
• in AS, master	Yes
• in AS, device	No
Digital inputs	
Number of digital inputs	24
of which inputs usable for technological functions	12
integrated channels (DI)	24
Input characteristic curve in accordance with IEC 61131, type 1	Yes
Number of simultaneously controllable inputs	
horizontal installation	
— up to 40 °C, max.	24
— up to 60 °C, max.	12
vertical installation	
— up to 40 °C, max.	12
Input voltage	
Rated value (DC)	24 V
• for signal "0"	-3 to +5V
• for signal "1"	+15 to +30 V
Input current	
• for signal "1", typ.	8 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.)
— Rated value	3 ms
for technological functions	
— at "0" to "1", max.	16 μs; Minimum pulse width/minimum pause between pulses at maximum counting frequency
Cable length	
• shielded, max.	1 000 m; 100 m for technological functions
• unshielded, max.	600 m; for technological functions: No
for technological functions	100 must maximum against for
— shielded, max.	100 m; at maximum count frequency
- unshielded, max.	not allowed
Digital outputs	
Number of digital outputs	16 4: Notice: You connect connect the fact outputs of your CDU in parallel
of which high-speed outputs	4; Notice: You cannot connect the fast outputs of your CPU in parallel
integrated channels (DO)	16 Voc: Clocked electronically
Short-circuit protection	Yes; Clocked electronically 1 A
Response threshold, typ. Limitation of inductive shutdown voltage to	1 A L+ (-48 V)
Controlling a digital input	L+ (-48 V) Yes
Switching capacity of the outputs	
on lamp load, max.	
	5 W
· · · · · · · · · · · · · · · · · · ·	5 W
Load resistance range	
Load resistance range ore limit	48 Ω
Load resistance range • lower limit • upper limit	
Load resistance range • lower limit • upper limit Output voltage	48 Ω 4 kΩ
Load resistance range • lower limit • upper limit Output voltage • for signal "1", min.	48 Ω
Load resistance range • lower limit • upper limit Output voltage	48 Ω 4 kΩ

a for signal "4" permissible range may	0.6.4
 for signal "1" permissible range, max. for signal "1" minimum load current 	0.6 A 5 mA
-	
for signal "0" residual current, max.	0.5 mA
Parallel switching of two outputs	No
 for uprating for redundant control of a load 	Yes
Switching frequency	165
with resistive load, max.	100 Hz
with inductive load, max.	0.5 Hz
 on lamp load, max. 	100 Hz
 of the pulse outputs, with resistive load, max. 	2.5 kHz
Total current of the outputs (per group)	
horizontal installation	
— up to 40 °C, max.	3 A
— up to 60 °C, max.	2 A
vertical installation	
— up to 40 °C, max.	2 A
Cable length	
• shielded, max.	1 000 m
• unshielded, max.	600 m
Analog inputs	
Number of analog inputs	4
For voltage/current measurement	4
• For resistance/resistance thermometer measurement	1
integrated channels (AI)	5; 4x current/voltage, 1x resistance
permissible input voltage for current input (destruction limit), max.	5 V; Permanent
permissible input voltage for voltage input (destruction limit), max.	30 V; Permanent
permissible input current for voltage input (destruction limit), max.	0.5 mA; Permanent
permissible input current for current input (destruction limit), max.	50 mA; Permanent
Electrical input frequency, max.	400 Hz
No-load voltage for resistance-type transmitter, typ.	3.3 V
Constant measurement current for resistance-type transmitter, typ.	1.25 mA
Technical unit for temperature measurement adjustable	Yes; Degrees Celsius / degrees Fahrenheit / Kelvin
Input ranges	
Voltage	Yes; ±10 V / 100 kΩ; 0 V to 10 V / 100 kΩ
• Current	Yes; ±20 mA / 100 Ω ; 0 mA to 20 mA / 100 Ω ; 4 mA to 20 mA / 100 Ω
Resistance thermometer	Yes; Pt 100 / 10 MΩ
Resistance	Yes; 0 Ω to 600 Ω / 10 M Ω
Input ranges (rated values), voltages	Vec
• 0 to +10 V	Yes
— Input resistance (0 to 10 V)	100 kΩ
Input ranges (rated values), currents • 0 to 20 mA	Vec
	Yes
 — Input resistance (0 to 20 mA) -20 mA to +20 mA 	100 Ω Yes
 -20 mA to +20 mA — Input resistance (-20 mA to +20 mA) 	Yes 100 Ω
 Input resistance (-20 mA to +20 mA) 4 mA to 20 mA 	Yes
 4 mA to 20 mA — Input resistance (4 mA to 20 mA) 	Tes 100 Ω
Input ranges (rated values), resistance thermometer	100.77
Pt 100	Yes
— Input resistance (Pt 100)	10 MΩ
Input ranges (rated values), resistors	
0 to 600 ohms	Yes
- Input resistance (0 to 600 ohms)	10 MΩ
Thermocouple (TC)	
Temperature compensation	
— parameterizable	No

Characteristic linearization	
parameterizable	Yes; by software
for resistance thermometer	Pt 100
Cable length	
• shielded, max.	100 m
Analog outputs	
	2
Number of analog outputs	2
integrated channels (AO)	
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit current, max.	55 mA
Current output, no-load voltage, max.	14 V
Output ranges, voltage	N
• 0 to 10 V	Yes
• -10 V to +10 V	Yes
Output ranges, current	N
• 0 to 20 mA	Yes
• -20 mA to +20 mA	Yes
• 4 mA to 20 mA	Yes
Connection of actuators	
for voltage output two-wire connection	Yes; Without compensation of the line resistances
for voltage output four-wire connection	No
for current output two-wire connection	Yes
Load impedance (in rated range of output)	
• with voltage outputs, min.	1 kΩ
 with voltage outputs, capacitive load, max. 	0.1 µF
• with current outputs, max.	300 Ω
 with current outputs, inductive load, max. 	0.1 mH
Destruction limits against externally applied voltages and currents	
 Voltages at the outputs towards MANA 	16 V; Permanent
• Current, max.	50 mA; Permanent
Cable length	
 shielded, max. 	200 m
Analog value generation for the inputs	
Measurement principle	Actual value encryption (successive approximation)
Measurement principle Integration and conversion time/resolution per channel	
Measurement principle	Actual value encryption (successive approximation) 12 bit
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable	
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference	12 bit
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released)	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms
Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max.	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel)	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit
Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time of resistive load	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms
Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms
Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time • for resistive load • for inductive load • for inductive load • for inductive load	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms
Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load for voltage measurement	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms
Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for inductive load for inductive load for voltage measurement for current measurement as 2-wire transducer	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes; with external supply
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time • for resistive load • for inductive load • for voltage measurement • for current measurement as 2-wire transducer	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes; with external supply Yes
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time • for resistive load • for inductive load • for outrent measurement • for current measurement as 2-wire transducer • for current measurement as 4-wire transducer	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes; with external supply Yes; with external supply Yes; Without compensation of the line resistances
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time • for resistive load • for inductive load • for voltage measurement • for current measurement as 2-wire transducer • for resistance measurement with two-wire connection • for resistance measurement with three-wire connection	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.6 ms 1 ms 0.5 ms Yes; with external supply Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time • for resistive load • for inductive load • for voltage measurement • for current measurement as 2-wire transducer • for resistance measurement with two-wire connection • for resistance measurement with four-wire connection	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes; with external supply Yes; with external supply Yes; Without compensation of the line resistances
Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for inductive load for capacitive load for ourrent measurement for current measurement as 2-wire transducer for resistance measurement with three-wire connection for resistance measurement with four-wire connection Connectable encoders	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.6 ms 1 ms 0.5 ms Yes; with external supply Yes Yes; Without compensation of the line resistances No No
Measurement principle Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Integration time, parameterizable • Interference voltage suppression for interference frequency f1 in Hz • Time constant of the input filter • Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time • for resistive load • for inductive load • for voltage measurement • for current measurement as 2-wire transducer • for resistance measurement with two-wire connection • for resistance measurement with four-wire connection	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.6 ms 1 ms 0.5 ms Yes; with external supply Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No

and activities 0.000 %/K Constant Reviewed in the input, min. 00.00 %/K Constant Reviewed in the input range, (r/-) 0.1 %/K Constant Reviewed in the input range, (r/-) 0.0 %/K Constant Reviewed in the input range, (r/-) 0.0 %/K Constant Reviewed in the input range, (r/-) 1 %/K Constant Reviewed input range, (r/-) 0.0 %/K Constant Reviewed input range, (r/-) <td< th=""><th>Errors/accuracies</th><th></th></td<>	Errors/accuracies	
Orderable Network the Insport, min. 00 dB Propert accurry in trady statil at 25 °C (reliative to input mapp.), (+). 0.08 %. Cubuch type (reliative to output mapp. Level to 00 %). 0.15 %. Linearity error (reliative to output mapp.), (+). 0.15 %. Constatile televen to output mapp. (+). 0.08 %. Constatile televen to output mapp. (+). 1 %. • Constant, reliative to input mapp. (+). 0.8 %. Linearity error s0.08 %. • Constant, reliative to input mapp. (+). 0.8 %. Linearity error s0.08 %. • Constant, reliative to input mapp. (+). 0.8 %. • Constant, reliative to input mapp. (+). 0.8 %. • Constant, reliative to input mapp. (+). 0.8 %. • Constant, reliative to input mapp. (+). 0.8 %. • Const		0.006 %/K
Proper accuracy in stacely state at 25 °C (reliative to input minip): (+') 0.00 %. Output ropie (reliative to output range): (+') 0.15 %. Temperature error (reliative to output range): (+') 0.15 %. Temperature error (reliative to output range): (+') 0.00 %. Output ropie (reliative to output range): (+') 0.01 %. Constatilic teative to input range): (+') 0.01 %. Propertion core roll into in overall temperature range 0.00 %. Output, rolling is rolling to input range, (+') 1 %. Corrent, relative to input range, (+') 1 %. Corrent, relative to input range, (+') 1 %. Ease: encline (incline call range, (+') 1 %. Corrent, relative to input range, (+') 0.8 %. Linearity error s0.08 %. Corrent, relative to input range, (+') 0.8 %. • lottings, relative to input range, (+') 0.8 %. • lottings, relative to input range, (+') 0.8 %. • lottings, relative to input range, (+') 0.8 %. • lottings, relative to input range, (+') 0.8 %. • lottings, relative to input range, (+') 0.8 %. • lottings, relative to input range, (+') 0.8 %.		
Curpt robe (relative to output range, bandwidth D to BI 442), (4) 0.1 %, Linearity error (relative to output range, (+/) 0.01 % A Crossink between the output, min. 60 dB Preparation correct (relative to output range, (+/) 0.06 %, Constraint, relative to input range, (+/) 1 %, • Voltage, relative to input range, (+/) 1 %, • Voltage, relative to input range, (+/) 1 %, • Voltage, relative to input range, (+/) 1 %, • Voltage, relative to input range, (+/) 1 %, • Current, relative to input range, (+/) 1 %, • Voltage, relative to input range, (+/) 1 %, • Voltage, relative to output range, (+/) 0 % (Linearity error ±0.05 %, • Output, relative to input range, (+/) 0 % (Linearity error ±0.05 %, • Voltage, relative to input range, (+/) 0 % (Linearity error ±0.05 %, • Output, relative to input range, (+/) 0 % (Linearity error ±0.05 %, • Output, relative to input range, (+/) 0 % (Linearity error ±0.05 %, • Output, relative to input range, (+/) 0 % (Linearity error ±0.05 %, • Output, relative to input range, (+/) 0 % (Linearity error ±0.05 %, • Output, relative to i	Repeat accuracy in steady state at 25 °C (relative to input	
Linearly error (relative to output range), (+1) 0.15 % Trapper ductive error (relative to output range), (+2) 0.05 % Constant hardware the outputs. 60 dB Repert accuracy in steady state at 25 °C (relative to output range), (+2) 0.05 % Constant and accuracy in steady state at 25 °C (relative to output range), (+2) 1 % Constant and the overall temperature range 1 % • Voltage, relative to input range, (+2) 1 % • Constant active to input range, (+2) 1 % • Constant active to input range, (+2) 1 % • Constant, relative to input range, (+2) 1 % • Constant, relative to input range, (+2) 0 % % Linearity error =0.06 % • Constant, relative to input range, (+2) 0 & % Linearity error =0.06 % • Constant, relative to input range, (+2) 0 & % Linearity error =0.06 % • Constant, relative to input range, (+2) 0 & % • Constant, relative to input range, (+2) 0 & % • Constant, relative to input range, (+2) 0 & % • Constant, relative to input range, (+2) 0 & % • Constant, relative to input range, (+2) 0 & % • Constriant inon constant Enterence, min. 40 dB	Output ripple (relative to output range, bandwidth 0 to 50 kHz),	0.1 %
Temperature area (reliable to output range). (+-) 0.01 %/kK Crosstalk between the outputs, min 60.08 Appart accurry in steady state at 25° (reliable to output range). (+-) 0.06 %. Outpace.(adulters to input range, (+-) 1 %. • Voltage, reliable to input range, (+-) 1 %. • Current, reliable to input range, (+-) 1 %. • Voltage, reliable to input range, (+-) 1 %. • Voltage, reliable to input range, (+-) 1 %. • Voltage, reliable to input range, (+-) 0.8 %; Linearity error ±0.06 %. • Voltage, reliable to input range, (+-) 0.8 %; Linearity error ±0.06 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage, reliable to input range, (+-) 0.8 %. • Voltage reliable to input ra		0.15 %
Constant between the output, min. 00.08 Prevent accurate in eleased said at 25 °C (relative to output only), (r/) 00.06 %. Operational error limb in overall temperature range 1 %. • Voltage, relative to input range, (r/) 1 %. • Outpace, relative to input range, (r/) 1 %. • Outpace, relative to input range, (r/) 1 %. • Outpace, relative to input range, (r/) 1 %. • Outpace, relative to input range, (r/) 0.8 %. Linearity error 10.06 %. • Outpace, relative to input range, (r/) 0.8 %. Linearity error 10.06 %. • Current, relative to input range, (r/) 0.8 %. Linearity error 10.06 %. • Current, relative to input range, (r/) 0.8 %. Linearity error 10.06 %. • Current, relative to input range, (r/) 0.8 %. • Current, relative to input range, (r/) 0.8 %. • Current, relative to input range, (r/) 0.8 %. • Current, relative to input range, (r/) 0.8 %. • Current, relative to input range, (r/) 0.8 %. • Current, relative to input range, (r/) 0.8 %. • Current, relative to input range, (r/) 0.8 %. • Current, relative to input range, (r/) 0.8 %. <td></td> <td></td>		
Prepret accuracy in steady state at 25 °C (relative to output array), (+) 0.00 % Operational error limit in overall temperature range • • Voltage, relative to input range, (+) 1 % • Current, relative to input range, (+) 1 % • Voltage, relative to input range, (+) 1 % • Voltage, relative to input range, (+) 1 % • Voltage, relative to input range, (+) 1 % • Voltage, relative to input range, (+) 0.8 %: Linearity error ±0.00 % • Current, relative to input range, (+) 0.8 %: Linearity error ±0.00 % • Current, relative to input range, (+) 0.8 %: Linearity error ±0.00 % • Current, relative to input range, (+) 0.8 %: Linearity error ±0.00 % • Voltage, relative to input range, (+) 0.8 %: Linearity error ±0.2 % • Voltage, relative to input range, (+) 0.8 % • Voltage, relative to input range, (+) 0.8 % • Voltage, relative to input range, (+) 0.8 % • Voltage, relative to input range, (+) 0.8 % • Voltage relative to input range, (+) 0.8 % • Voltage relative to input range, (+) 0.8 % • Voltage relative to input range, (+) 0.8 %		
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• Voltage, relative to output range, (+/-) 1 % • Current, relative to input range, (+/-) 0.8 %; Lineardy error ±0.06 % • Current, relative to input range, (+/-) 0.8 %; Lineardy error ±0.06 % • Current, relative to input range, (+/-) 0.8 %; Lineardy error ±0.06 % • Resistance, relative to input range, (+/-) 0.8 %; Lineardy error ±0.06 % • Voltage, relative to input range, (+/-) 0.8 %; Lineardy error ±0.06 % • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.8 %; • Voltage, relative to input range, (+/-) 0.0 %; • Voltage, relative to	• Resistance, relative to input range, (+/-)	1 %
e. Current, relative to output range, (+-) 0.8 %; Linearity error ±0.06 % Current, relative to input range, (+-) 0.8 %; Linearity error ±0.06 % - Current, relative to input range, (+-) 0.8 %; Linearity error ±0.06 % - Resistance thermometer, relative to input range, (+-) 0.8 %; Linearity error ±0.06 % - Resistance thermometer, relative to input range, (+-) 0.8 %; Linearity error ±0.2 % - Resistance thermometer, relative to input range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Current, relative to output range, (+-) 0.8 % - Morent of Resistance 0.0 Number of Resistance 0.0 <t< td=""><td> Voltage, relative to output range, (+/-) </td><td>1 %</td></t<>	 Voltage, relative to output range, (+/-) 	1 %
Basic error limit (operational limit at 25 °C) 0.8 %: Linearity error 10.06 % • Voltage, relative to input range, (+/-) 0.8 %: Linearity error 10.06 % • Courrent, relative to input range, (+/-) 0.8 %: Linearity error 10.06 % • Resistance, relative to input range, (+/-) 0.8 % • Resistance, relative to input range, (+/-) 0.8 % • Voltage, relative to output range, (+/-) 0.8 % • Current, relative to input range, (+/-) 0.8 % • Thereforence Voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage suppression for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11 = interforence voltage superssion for 1 = n x (11 + 1 %), 11		1 %
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• Current, relative to input range, (+-) 0.8 %; Linearity error ±0.06 % • Resistance, relative to input range, (+-) 0.8 % • Voltage, relative to output range, (+-) 0.8 % • Current, relative to output range, (+-) 0.8 % • Current, relative to output range, (+-) 0.8 % • Current, relative to output range, (+-) 0.8 % • Current, relative to output range, (+-) 0.8 % • Current, relative to output range, (+-) 0.8 % • Courrent, relative to output range, (+-) 0.8 % • Courrent, relative to output range, (+-) 0.8 % • Common mode interference, min. 40 dB • Mumber of findustrial Ethernet interfaces 0 • Number of RS 485 interface, max. 200 mA • Protocols 200 mA • Protocols 187.5 kbt/s • Protocols No • Protocols		0.8 %; Linearity error ±0.06 %
• Resistance, relative to input range, (+/-) 0.8 %; Linearity error ±0.2 % • Resistance thermometer, relative to input range, (+/-) 0.8 % • Current, relative to output range, (+/-) 0.8 % • Interference voltage suppression for I = n x (ff +/-1 %), ff = interference receptory 30 dB • Series mode interference (peak value of interference < 0		· · · ·
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Interforence voltage suppression for f = n x (f1 +/- 1 %), f1 = interforence frequency 30 dB • Series mode interference (peak value of interference < rated value of input range), min.		
• Series mode interference (pask value of interference < rated value of input range), min. 30 dB • Common mode interference, min. 40 dB Number of Rotatial Ethernet interfaces 0 Number of PROFINET interfaces 0 Number of RS 485 interfaces 1: MPI Number of RS 482 interfaces 0 1. Interface 0 Interface type Integrated RS 485 interface Isolated No Interface type Integrated RS 485 interface Isolated No Interface types Ves • RS 485 Yes • Output current of the interface, max. 200 mA Protocols Ves • PROFIBUS DP master No • PROFIBUS DP device No • Protocols Yes - S7 communication Yes - S7 basic communica		
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• RS 485 Yes • Output current of the interface, max. 200 mA Protocols		
• Output current of the interface, max. 200 mA Protocols		Vec
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• MPI Yes • PROFIBUS DP master No • PROFIBUS DP device No • Point-to-point connection No MPI		
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PG/OP communication Yes Data record routing No Global data communication Image: Communication	PROFIsafe	No
Data record routing No Global data communication No	communication functions / header	
Global data communication	PG/OP communication	Yes
	Data record routing	No
• supported Yes	Global data communication	
	supported	Yes

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 Number of GD loops, max. 	8
 Number of GD packets, max. 	8
 Number of GD packets, transmitter, max. 	8
 Number of GD packets, receiver, max. 	8
 Size of GD packets, max. 	22 byte
 Size of GD packet (of which consistent), max. 	22 byte
S7 basic communication	
 supported 	Yes
 User data per job, max. 	76 byte
• User data per job (of which consistent), max.	76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET as server)
S7 communication	
supported	Yes
as server	Yes
• as client	Yes; Via CP and loadable FB
• User data per job, max.	180 byte; With PUT/GET
 User data per job (of which consistent), max. 	240 byte; as server
S5 compatible communication	
supported	Yes; via CP and loadable FC
Number of connections	
• overall	8
usable for PG communication	7
- reserved for PG communication	1
— adjustable for PG communication, min.	1
- adjustable for PG communication, max.	7
usable for OP communication	7
- reserved for OP communication	1
— adjustable for OP communication, min.	1
— adjustable for OP communication, max.	7
 usable for S7 basic communication 	4
 usable for S7 basic communication 	
- reserved for S7 basic communication	0
 reserved for S7 basic communication adjustable for S7 basic communication, min. 	0 0
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. 	
 reserved for S7 basic communication adjustable for S7 basic communication, min. 	0
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. 	0
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions 	0 4 8; Depending on the configured connections for PG/OP and S7 basic
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max.	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max.	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300
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 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes
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 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes 1 Yes 30 30 30 30
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 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. of which status variables, max. Forcing	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. of which status variables, max. Forcing Forcing 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14 Yes
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 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. of which status variables, max. Forcing Forcing Forcing, variables Number of variables, max. Diagnostic buffer present 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14 Yes Inputs, outputs 10 Yes
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. of which status variables, max. of which control variables, max. Forcing Forcing Forcing, variables Number of variables, max. Diagnostic buffer present Number of entries, max. 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14 Yes Inputs, outputs 10 Yes 500
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. of which status variables, max. of which control variables, max. of which control variables, max. Jorcing Forcing Forcing, variables Number of variables, max. Diagnostic buffer present Number of entries, max. adjustable 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes 1nputs, outputs, memory bits, DB, times, counters 30 30 14 Yes Inputs, outputs 10 Yes 10 10 Yes
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 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. of which status variables, max. of which control variables, max. Forcing Forcing Forcing, variables Number of variables, max. Diagnostic buffer present Number of entries, max. of which powerfail-proof Number of entries readable in RUN, max. 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14 Yes Inputs, outputs 10 Yes 500 No 100; Only the last 100 entries are retained 499
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. of which status variables, max. of which control variables, max. of which control variables, max. forcing Forcing Forcing, variables, max. Diagnostic buffer present Number of entries, max. of which powerfail-proof 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes Yes; Up to 2 simultaneously Yes 4 Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14 Yes Inputs, outputs 10 Yes 500 No 100; Only the last 100 entries are retained 499 Yes; From 10 to 499
 reserved for S7 basic communication adjustable for S7 basic communication, min. adjustable for S7 basic communication, max. S7 message functions Number of login stations for message functions, max. Process diagnostic messages simultaneously active Alarm-S blocks, max. Test commissioning functions Status block Single step Number of breakpoints Status/control Status/control variable Variables Number of variables, max. of which status variables, max. of which control variables, max. Forcing Forcing Forcing, variables Number of variables, max. Diagnostic buffer present Number of entries, max. of which powerfail-proof Number of entries readable in RUN, max. 	0 4 8; Depending on the configured connections for PG/OP and S7 basic communication Yes 300 Yes; Up to 2 simultaneously Yes 4 Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14 Yes Inputs, outputs 10 Yes 500 No 100; Only the last 100 entries are retained 499

can be read out	Yes
Interrupts/diagnostics/status information	
Diagnostics indication LED	
Status indicator digital input (green)	Yes
Status indicator digital nuput (green)	Yes
Integrated Functions	
Frequency measurement	Yes
Number of frequency meters	3; up to 30 kHz (see "Technological Functions" manual)
controlled positioning	No
integrated function blocks (closed-loop control)	Yes; PID controller (see "Technological Functions" manual)
PID controller	Yes
Number of pulse outputs	3; Pulse width modulation up to 2.5 kHz (see "Technological Functions"
	Manual)
Limit frequency (pulse)	2.5 kHz
Potential separation	
Potential separation digital inputs	
 Potential separation digital inputs 	Yes
between the channels	No
 between the channels and backplane bus 	Yes
Potential separation digital outputs	
Potential separation digital outputs	Yes
between the channels	Yes
 between the channels, in groups of 	8
between the channels and backplane bus	Yes
Potential separation analog inputs	
Potential separation analog inputs	Yes; common for analog I/O
between the channels	No
 between the channels and backplane bus 	Yes
Potential separation analog outputs	
 Potential separation analog outputs 	Yes; common for analog I/O
between the channels	No
 between the channels and backplane bus 	Yes
Isolation	
Isolation tested with	600 V DC
Ambient conditions	
Ambient temperature during operation	
• min.	0 °C
• max.	0° 00°C
configuration / header	
Configuration software	
STEP 7	Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP
	203
STEP 7 Lite	
	No
Origination / programming / header	No
	No see instruction list
configuration / programming / header	
configuration / programming / header • Command set	see instruction list
configuration / programming / header • Command set • Nesting levels	see instruction list 8
configuration / programming / header • Command set • Nesting levels • System functions (SFC)	see instruction list 8 see instruction list
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB)	see instruction list 8 see instruction list
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language	see instruction list 8 see instruction list see instruction list
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language — LAD	see instruction list 8 see instruction list see instruction list
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD	see instruction list 8 see instruction list see instruction list Yes Yes
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD - STL	see instruction list 8 see instruction list see instruction list Yes Yes Yes
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD - STL - SCL	see instruction list 8 see instruction list see instruction list Yes Yes Yes Yes
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD - STL - SCL - CFC	see instruction list 8 see instruction list see instruction list Yes Yes Yes Yes Yes
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD - STL - SCL - CFC - GRAPH - HiGraph®	see instruction list 8 see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD - STL - SCL - CFC - GRAPH - HiGraph® Know-how protection	see instruction list 8 see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD - STL - SCL - CFC - GRAPH - HiGraph® Know-how protection • User program protection/password protection	see instruction list 8 see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD - STL - SCL - CFC - GRAPH - HiGraph® Know-how protection • Block encryption	see instruction list 8 see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes
configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language - LAD - FBD - STL - SCL - CFC - GRAPH - HiGraph® Know-how protection • User program protection/password protection	see instruction list 8 see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes

660 g	
130 mm	
125 mm	
	130 mm